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PB

IN THE UNITED STATES PATENT & TRADEMARK OFFICE

IN RE APPLICATION OF: :

Warren SNYDER   : GROUP ART UNIT:

SERIAL NO: 09/972,319   :

FILED: OCTOBER 5, 2001   : EXAMINER:

FOR: METHOD FOR APPLYING
        INSTRUCTIONS TO
        MICROPROCESSOR IN TEST
        MODE

PRELIMINARY AMENDMENT

COMMISSIONER FOR PATENTS
WASHINGTON, D.C. 20231

SIR:

Prior to examination of the above-identified application, consideration of the following

Amendments and Remarks is respectfully requested.

IN THE SPECIFICATION

Please replace the paragraphs from page 10, line 22, through page 11, line 6 with the

following paragraphs:

The peripheral registers 230 may be, in general, those components which constitute parts

of a microcontroller. For example, these registers may comprise programmable analog and digital

on-chip systems, input/output pins, and various fixed function blocks.

Thus, embodiments of the present invention provide for an efficient mechanism for feeding

instructions to the microprocessor 210, single stepping instructions, programming the